

PATENT NUMBER

05-0203

<b>P.D.</b>	<b>O.I.P.E.</b>	<b>PATENT DATE</b>
<b>SCANNED</b>	<b>Q.A.</b>	

APPLICATION NO. 09/669034	CONT/PRIOR	CLASS 718 710	SUBCLASS 260	ART UNIT 2181	EXAMINER KIMBLE
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Method and apparatus for maintaining the temperature of a microprocessor using a firmware-assisted interrupt mechanism

PTO-2040  
12/89

<b>ISSUING CLASSIFICATION</b>															
<b>ORIGINAL</b>								<b>CROSS REFERENCE(S)</b>							
<b>CLASS</b>				<b>SUBCLASS</b>				<b>CLASS</b>				<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>			
<b>INTERNATIONAL CLASSIFICATION</b>															
								<input type="checkbox"/> Continued on Issue Slip Inside File Jacket							

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		_____ (Date)	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  _____  _____	_____   _____ (Primary Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
			<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		_____ (Date)	
			<b>ISSUE BATCH NUMBER</b>	

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